

(19)



Europäisches Patentamt
European Patent Office
Office européen des brevets

(11) Publication number:

0 354 369
A2

(12)

EUROPEAN PATENT APPLICATION

(21) Application number: 89112731.8

(51) Int. Cl.4: **G01J 5/20** , **H05K 1/16** ,
//G08B13/18

(22) Date of filing: 12.07.89

(30) Priority: 12.08.88 US 231797

(43) Date of publication of application:
14.02.90 Bulletin 90/07

(84) Designated Contracting States:
DE FR GB IT

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(54) Infrared detector.

(57) Preferred embodiments include a monolithic uncooled infrared detector array of bolometers fabricated over a silicon substrate (142); the bolometers include a stack (144) of oxide (146), TiN (148), a-Si:H (150), TiN (152), oxide (154) with the TiN forming the infrared absorbers and resistor contacts and the a-Si:H the resistor with a high temperature coefficient of resistivity. The resistor is suspended over the silicon substrate (142) by metal interconnects (154 and 156) and related processing circuitry is formed in the silicon substrate (142) beneath the resistor.

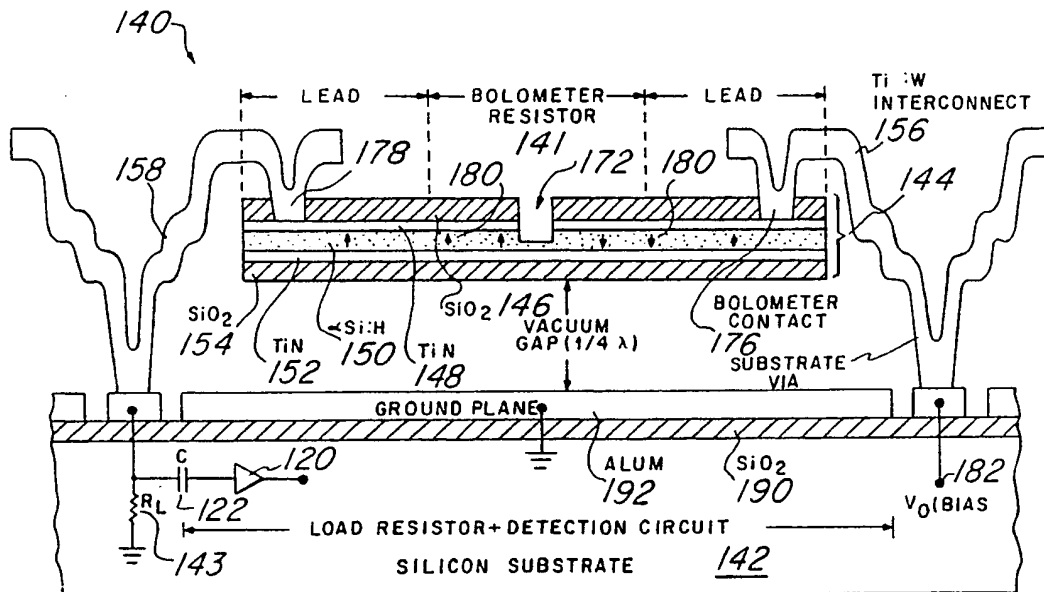


Fig. 4a

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INFRARED DETECTOR

BACKGROUND OF THE INVENTION

1. Field of the Invention.

The present invention relates to infrared-radiation detectors, and, more particularly, to uncooled bolometers for detection of infrared radiation.

2. Description of the Related Art.

Detection of infrared radiation emitted by warm bodies provides an important method for night vision (perception without visible light). Infrared detectors may be classified in various ways, such as scanning or staring arrays, cryogenic (typically liquid nitrogen temperatures) or uncooled detectors, 3-5 micron or 8-12 micron spectral sensitivity range, and photon or thermal detection mechanism. Cryogenic infrared detectors are typically made of small bandgap (about 0.1-0.2 eV) semiconductors such as HgCdTe and operate as photodiodes or photocapacitors by photon absorption to produce electron hole-pairs. For example, see U.S.Pat.No. 4,686,373 (Tew et al) which describes a hybrid system with HgCdTe photocapacitors glued to silicon signal processing circuitry.

Uncooled infrared detectors cannot make use of small bandgap semiconductors because the bandgap is only about $4kT$ at room temperature and dark current swamps any signal. Consequently, uncooled infrared detectors rely on other physical phenomena and are less sensitive than cryogenic detectors but do not require cooling apparatus or its energy consumption. For portable, low power applications where the greater detectivity of cryogenic detectors is not needed, the preferred choice is an uncooled thermal detector: infrared photons are absorbed and the resultant heating of the absorbing element is detected. The thermal detector is usually one of three types: (1) pyroelectric detector, (2) thermocouple, or (3) bolometer.

The pyroelectric detector uses a ferroelectric ceramic material (such as BaSrTiO₃) at operating temperatures (typically 0 C to 150 C) a little below its Curie temperature. The preferred ferroelectric materials have a large change in spontaneous dielectric polarization at operating temperatures, and the heating of the ferroelectric is detected by sensing the induced voltage created by the generation of charge across a capacitor with the ferroelectric as insulator. For example, see U.S.Pat.Nos. 4,348,611 (Ruppel et al), 4,142,207 (McCormack et al), and 4,379,232 (Hopper).

The pyroelectric detector is a hybrid approach that has problems when extended to large detector arrays (arrays of 256 by 256 pixels) due to ferroelectric material defects, contact defects, and bump bonding defects which imply low yields.

Thermocouples rely on the change in contact potential of a junction of dissimilar conductors with temperature; see for example G.Lahiji et al, A Monolithic Thermopile Detector Fabricated Using Integrated-Circuit Technology, 1980 IEEE IEDM Tech.Dig. 676 which uses bismuth-antimony or polysilicon-gold couples in monolithic arrays on silicon.

Bolometers typically rely on the temperature change of resistance of thermally isolated thin metal films or semiconductor films. The thin films may be fabricated on a suspended dielectric film in a silicon substrate and may be located adjacent to monolithic detection circuitry on the silicon substrate. The dielectric film is thermally isolated from the remainder of the silicon substrate by etching away the silicon from beneath the dielectric film so that it is suspended. See for example K.C.Liddiard, 24 Infrared Physics 57 (1984) and 26 Infrared Physics 43 (1986) which includes amorphous silicon films for resistance change with temperature plus abutting nickel films for infrared absorption and electrical contact. The foregoing references are hereby incorporated by reference.

Thermocouples and bolometers avoid the yield problem of the pyroelectric detectors because they can be monolithically fabricated on a silicon wafer along with the detection circuitry. However, thermocouples and bolometers have the problem of lower detectivities than pyroelectric detectors.

SUMMARY OF THE INVENTION

The present invention provides infrared radiation imagers in the form of a monolithic array of

bolometers with a high fill factor geometry together with high sensitivity resistance material. Preferred embodiments use doped, hydrogenated amorphous silicon (a-Si:H) coated with an infrared absorber as the resistance material and an architecture suspending the resistance material over silicon detection circuitry. The distance between the suspended resistance material and the underlying reflective substrate is one-quarter of the wavelength of the center of the infrared spectral band and provides enhanced infrared absorption.

This overcomes the problems of the known uncooled thermal detectors by achieving detectivity comparable to pyroelectric detectors while retaining the monolithic fabrication advantages of bolometers.

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BRIEF DESCRIPTION OF THE DRAWINGS

The drawings are schematic for clarity.

Figure 1 shows a block diagram of an infrared detector system which includes a first preferred embodiment bolometer array;

Figure 2 is a plan view of a first preferred embodiment array;

Figure 3 is a perspective view of a single bolometer of the first preferred embodiment array;

Figures 4a-b are cross sectional elevational views of a single bolometer of the first preferred embodiment array;

Figures 5a-b are plan views of a single bolometer of the first preferred embodiment array and a portion of the array;

Figure 6 illustrates a model for analysis of the operation of the first preferred embodiment;

Figure 7 shows theoretical absorption, reflection and transmission curves;

Figures 8a-g are cross sectional elevational views of steps of a preferred embodiment method of fabrication; and

Figures 9a-e are plan views of further preferred embodiment single bolometers.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

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Figure 1 illustrates in schematic block diagram view an infrared imager system, generally denoted 100, which includes infrared lens system 102, mechanical chopper 104, bolometer array 106, drive and read out electronics 108 for array 106, video processor 110, display 112, and timing and control electronics 114. Lens system 102 images infrared radiation emitted by warm object 116 onto array 106. Chopper 104 may be a rotating disk with openings that periodically block and let pass the infrared radiation collected by lens system 102. Array 106 includes 64,536 bolometers arranged into 256 rows by 256 columns; each bolometer corresponds to a pixel in the image of warm body 116 plus surrounding scene in the field of view of lens system 102. Array 106 is housed in a stainless steel vacuum chamber with a infrared transparent window formed in one side. The window is positioned so that radiation from the scene with warm body 116 passes through the window onto array 106.

Figure 2 schematically illustrates in plan view a portion (four bolometers) of array 106 with thermally isolated resistor 141 that has a resistance R_B that varies with temperature and is connected between grounded fixed load resistor 143 with resistance R_L and fixed bias voltage V_0 .

Figure 3 is a schematic perspective view of a single bolometer, generally denoted by reference numeral 140, from array 106. The operation of array 106 is as follows: The chopped scene including warm body 116 is imaged onto array 106 which causes each R_B to fluctuate in value (with magnitude proportional to the temperature of the corresponding portion of the scene); the fluctuating value of R_B yields an alternating voltage across load resistor 143 which is fed to buffer amplifier 120 through capacitor 122. The outputs of buffer amplifiers 120 are read in column read out circuit 128 with a row at a time selected by row addressing circuit 130 which turns on pass transistors 132 in one row at a time.

Figures 4a-b are schematic cross sectional elevational views of bolometer 140 and Figure 5a is a plan view; Figure 5b illustrates in plan view a portion of array 106 showing the arrangement of the individual bolometers. Bolometer 140 includes a 1,700 Å thick and 50 microns square stack 144 made of a top 500 Å layer of silicon dioxide (oxide) 146, a 100 Å layer of titanium nitride (TiN) 148, a 500 Å layer of hydrogenated amorphous silicon (a-Si:H) 150 doped with boron to a carrier concentration of $2 \times 10^{18}/\text{cm}^3$, another 100 Å layer of TiN 152, and a bottom 500 Å layer of oxide 154. Stack 144 is supported over substrate 142 by two titanium tungsten (Ti:W) interconnects 156 and 158 located at diagonally opposite corners of the stack 144. As shown in Figure 5a, stack 144 is square with two pairs at elongated openings

160, 162 and 164, 166 defining leads 170 and 174 between interconnects 156 and 158 and the remainder of stack 144 (resistor 141). Leads 170 and 174 are about 22 microns long and about 1.5 microns wide; this provides high thermal resistance. However, the heat loss from the main portion of stack 144 through leads 170 and 174 is more than an order of magnitude larger than the black-body radiation heat loss due to the temperature differential between resistor 141 and substrate 142. Upper electrode gap 172 separates upper oxide layer 146 and upper TiN layer 148 into two portions: one portion connected to interconnect 156 at contact 176 and the other portion connected to interconnect 158 at contact 178.

Vertical arrows 180 illustrate the direction of the direct current flow through a-Si:H 150. The current flow is from bias voltage supply 182 (5.8 volts) on substrate 142 through metal interconnect 156, into the half of TiN layer 148 on the contact 176 side of electrode gap 172, vertically down through a-Si:H 150 into TiN layer 152, vertically up through a-Si:H 150 into the half of TiN layer 148 on the contact 178 side of electrode gap 172, and lastly through interconnect 158 and load resistor 143 in substrate 142 to ground. The halves of TiN layer 148 and TiN layer 152 are highly conductive (about 800 $\mu\Omega$ -cm resistivity) relative to a-Si:H 150 (about 1 M Ω -cm resistivity) and may be considered equipotential surfaces. Thus resistor 141 is essentially two a-Si:H resistors in series, each a a-Si:H resistor with a 500 Å length and a cross sectional shape of a 50 micron edge right triangle having a narrow notch extending from the right angle vertex nearly 90% of the way to the hypotenuse. The total resistance, R_B , is about 1.1 M Ω and R_L is taken to be the same, so the total array 106 power dissipation in the resistors 141 and load resistors 143 is about 1 watt. The temperature coefficient of resistivity of a-Si:H 150 is about -0.072 per degree C at a 300 degrees K operating temperature of array 106.

The thermal time constant for resistor 141 is smaller than the period of chopper 104 for good sensitivity; this is analyzed *infra*. The cooling of resistor 141 is primarily by heat conduction through leads 170 and 174 and then through supports 156 and 158 to silicon substrate 142. In particular, Figure 6 models the heat flow for resistor 141 with P the incident infrared radiant power, ϵ the fraction of the incident radiant power absorbed, T the temperature of resistor 141, T_m the mean temperature of resistor 141, M the mass of resistor 141, c the specific heat of resistor 141, R_B the resistance of resistor 141, k the thermal conductivity of the leads 170 and 174, a the cross sectional area of leads 170 and 174, l the length of leads 170 and 174, T_0 the temperature of substrate 142, V_0 the applied dc voltage, α the temperature coefficient of resistance of resistor 141, R_L the resistance of load resistor 143, and V_S the signal voltage.

The power input to resistor 141 is the absorbed radiant power (ϵP) plus the resistive heating power from V_0 ($i^2 R_B$), and the power loss from resistor 141 is primarily heat conduction down the leads 170 and 174 due to the thermal gradient ($ka \frac{dT}{dx}$) along the leads. The gradient may be approximated by a linear temperature change along the leads, so the power loss is $(k(a/l)(T - T_0))$. The net power input to resistor 141 causes a change in the temperature:

$$\epsilon P + i^2 R_B - K(T - T_0) = C \frac{dT}{dt}$$

where $C = Mc$ is the thermal capacitance of R_B and $K = k(a/l)$ is the thermal conductance of leads 170 and 174. Note that both i and R_B depend upon T ; however, a linear approximation to the temperature dependence of R_B makes the differential equation tractable. In particular, presume the temperature coefficient of resistance

$$\alpha \left(= \frac{1}{R_B} \frac{dR_B}{dT} \right)$$

is constant over the range of fluctuations of T . Typically, T_0 is about 300 degrees K, T_m is about 315-320 degrees K, and the fluctuation of T is much less than one degree K about T_m , so α constant is not a stringent presumption. For a-Si:H 150 α is about -0.072 at 320 degrees K, hence $\alpha(T - T_m)$ is small and a first order approximation is sufficiently accurate:

$$i^2 R_B = \frac{V_0^2 R_m}{(R_L + R_m)^2} \left[1 + \alpha(T - T_m) \left(\frac{R_L - R_m}{R_L + R_m} \right) \right]$$

where R_m is the value of R_B at temperature T_m , in other words, the mean resistance. The differential equation thus becomes:

$$C \frac{dT}{dt} + K(T - T_0) - \alpha(T - T_m) \frac{V_0^2 R_m}{(R_L + R_m)^2} \frac{R_L - R_m}{R_L + R_m} = \epsilon P + \frac{V_0^2 R_m}{(R_L + R_m)^2}$$

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where T and P are the only functions of time, t . Rearranging the differential equation yields:

$$C \frac{d(T - T_m)}{dt} + (T - T_m) \left[K - \alpha \frac{V_0^2 R_m}{(R_L + R_m)^2} \frac{R_L - R_m}{R_L + R_m} \right] = \epsilon P + \frac{V_0^2 R_m}{(R_L + R_m)^2} - (T_m - T_0)K$$

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where the second two terms on the right side of the equation are large (the heating power due to the dc current and the cooling power by conduction through the leads) and substantially cancel, and the variables $(T - T_m)$ and P are small.

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The radiant power input is chopped to improve signal-to-noise ratio. Thus the input radiant power is a square wave, and the resulting signal voltage can be computed from the foregoing equation to estimate the properties of bolometer array 106. The differential equation is easily solved by Fourier series expansion of P and $T - T_m$ and equating coefficients. That is, take P to be given by

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$$P(t) = P_0 \left[\frac{1}{2} + \frac{2}{\pi} \sin(\omega t) + \frac{2}{3\pi} \sin(3\omega t) + \dots \right]$$

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and T to be

$$T(t) = T_m + T_\omega \sin(\omega t - \phi) + T_{3\omega} \sin(3\omega t - \psi) + \dots$$

where ω is the angular frequency of the chopping of the incident radiant power by chopper 104. The zero order coefficients yield:

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$$0 = \epsilon P_0 \frac{1}{2} + \frac{V_0^2 R_m}{(R_L + R_m)^2} - (T_m - T_0)K$$

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The first order coefficients yield:

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$$T_\omega = \frac{\epsilon P_0 \frac{2}{\pi}}{\sqrt{\omega^2 C^2 + K_{eff}^2}}$$

with

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$$K_{eff} = \left[K - \alpha \frac{V_0^2 R_m}{(R_L + R_m)^2} \frac{R_L - R_m}{R_L + R_m} \right]$$

and

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$$\phi = \arctan \frac{\omega C}{K_{eff}}$$

Note that neglecting ϵP_0 in the zero order equation and inserting the result in the expression for K_{eff} yields:

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$$K_{eff} = K \left[1 - \alpha(T_m - T_0) \frac{R_L - R_m}{R_L + R_m} \right]$$

so K_{eff} is an effective heat conductance. Also, for chopping angular frequencies greater than K_{eff}/C the amplitude of the fluctuations of T will decrease approximately inversely to ω .

The second order coefficients (and all other even-order coefficients) vanish, and for higher odd-order coefficients $T_{n\omega}$ falls off like $1/n^2$ because $P_{n\omega} = P_0 \frac{2}{n\pi}$ and the derivative contributes another $1/n$. Thus approximate T by $T_m + T_\omega \sin(\omega t - \phi)$. This approximation, of course, is the exact solution for $P = P_0(\frac{1}{2} + \frac{2}{\pi} \sin \omega t)$ which are just the first two terms of the Fourier expansion of the square wave.

The output signal V_S due to incident P is computed by inserting the resulting T , and thus R_B , into the definition of V_S as the fluctuation of the voltage across R_L :

$$V_S = V_0 \left[\frac{R_L}{R_B + R_L} - \frac{R_L}{R_m + R_L} \right]$$

which gives a first order approximation of

$$V_S = -IR_m \alpha \frac{R_L}{R_L + R_m} T_\omega \sin(\omega t - \phi)$$

The response of the detector (the amplitude of V_S divided by average incident power P_0) therefore increases with increasing I (the dc current), R_m , α , and decreases with increasing C and K_{eff} (which decreases T_ω). Because the input impedance of amplifier 120 has to be larger than R_B , this puts an upper limit on R_B . Since any fluctuation of I causes a noise signal, the current I through bolometer 140 has to be extremely constant. This, and the requirement that the temperature rise by Joule heating should be small limits the maximum current I . K_{eff} has a lower limit in the sense that even with perfect thermal isolation of resistor 141, a temperature difference of resistor 141 from substrate 142 gives a net radiation flux from resistor 141 to substrate 142.

The choice of R_L to maximize the sensitivity of bolometer 140 is easily computed from the definition of V_S by noting that this is just maximizing

$$\frac{\partial V_S}{\partial R_B} \quad \text{at} \quad R_B = R_m$$

as a function of R_L . This yields $R_L = R_m$. With this value of R_L , $K_{eff} = K$, $T_\omega = \epsilon P_0 \frac{2}{\pi} / \sqrt{\omega^2 C^2 + K^2}$, and $V_S = -\frac{1}{2} / V_0 \alpha T_\omega \sin(\omega t - \phi)$ which is independent of R_m .

The temperature response to a step input incident radiant power may also be computed by solving the first order approximation differential equation: Presume $P = 0$ for $t < 0$ and $P = P_0$ for $t > 0$, then

$$T = T_\infty - (T_\infty - T_0)e^{-t/\tau} \quad \text{with} \quad \tau = \frac{C}{K_{eff}}$$

where T_∞ is the asymptotic temperature and T_0 is the temperature prior to illumination. Thus the time constant of bolometer 140 is τ .

To maximize the signal voltage amplitude (which is the responsivity multiplied by the incident power), bolometer 140 should have a minimum thermal capacitance C and thermal conductance K and maximum active resistor absorbing area A , temperature coefficient α , and absorption ϵ . Bolometer 140 approaches these goals as follows. The thermal capacitance C is minimized by making stack 144 of thin films and the thermal conductance K is minimized by making leads 170 and 174 long and narrow. The active area A is maximized by integrating array 106 on a single silicon substrate and locating the detection circuitry under resistor 141 to provide high fill factor in array 106. The temperature coefficient α for thermister-type materials may be as high as 0.75 per degree K, but these materials are incompatible with silicon processing, and metal alloy films have coefficients of less than 0.01. So doped a-Si:H with a coefficient of about 0.07 provides a high coefficient together with silicon processing compatibility. Amorphous silicon is transparent to infrared radiation, so a high absorption ϵ is achieved by having the TiN resistor electrodes

148 and 152 also provide the absorption. This is free carrier absorption and can be optimized by choice of resistivity, thickness, and gap from stack 144 to ground plane 192. The analysis is as follows.

First, consider infrared radiation normally incident on a metal film of thickness t and resistivity ρ , and surrounded by air on both sides. The absorption A , reflection R , and transmission T are illustrated in Figure 7 and computed from Maxwell's equations with the presumption that the index of refraction and the extinction coefficient of the metal are nearly equal, which is the case for wavelengths $\lambda \geq 10 \mu\text{m}$. The absorption has a maximum at $120\pi t/\rho = 2$.

Next, consider the addition of a thick conducting layer parallel to the metal film and at a distance of $\lambda/4$ away. Then nearly 100% absorption is achieved at $120\pi t/\rho = 1$. This analysis appears in L.Hadley et al, 37 J.Optical Soc. Am. 451 (1947).

For bolometer 140 operation with 8 to 12 micron spectral sensitivity, the center wavelength is 10 microns so the the distance from electrodes 148 and 152 to ground plane 192 is set at 2.5 microns (one quarter wavelength). The $120\pi t/\rho = 1$ requirement applies to the combination of layers 148 and 150 because the thickness of the transparent a-Si:H is small compared to the wavelength. Thus with a total thickness of 200 Å, the resistivity should be equal to $120\pi \times 2 \times 10^{-6}$, or about 750 $\mu\Omega\text{-cm}$, and this is about the resistivity of reactively sputtered TiN. For the 100 Å thick TiN electrodes 148 and 152 this translates to a sheet resistance of about 750 Ω/\square . Note that other materials, including unannealed, sputtered silicides such as MoSi₂ and WSi₂, also are compatible with silicon processing and have appropriate resistivities. Because only the ratio of resistivity-to-thickness enters into the absorption analysis, the thickness of the electrodes 148 and 152 could be adjusted to compensate for the resistivity of the material used provided that the layer thickness is not so great as to significantly increase the thermal capacitance C nor so small as to yield nonreproducible film properties. Also, oxide 146 and 154 provide a sealing of TiN 148 and 152 so that the TiN is never subjected to any plasma etches, oxidation or hydration effects which might affect its resistivity. Stable sheet resistances for the TiN layers are important in maintaining the optimum infrared absorption and uniformity of absorption.

As shown in Figures 4a-b, oxide layer 190 covers and isolated load resistor R_L and buffer amplifier 120 and the remainder of silicon substrate 142 from aluminum ground plane 192. The distance from aluminum ground plane 192 to the bottom of stack 144 is about 2.5 microns for de tecton in the 8-12 micron wavelength range; this distance from ground plane 192 to stack 144 is one quarter wavelength for the range's center frequency. Figure 4a schematically shows ground plane 192 as flat; whereas, Figure 4b illustrates some of the underlying circuitry (CMOS) in substrate 142 and indicates the slight (few hundred Å) unevenness of ground plane 192. TiN layers 148 and 152 provide the absorption of incident infrared radiation from the scene with warm body 116; amorphous silicon is transparent to infrared radiation. The quarter wavelength distance from ground plane 192 to stack 144 creates a quarter wavelength absorption filter for free absorption in the thin, semi-transparent TiN 148 and 152 with an underlying quarter wavelength vacuum gap and ground plane reflector. The unevenness of ground plane 192 has minimal effect on the absorption. Note that use of a vacuum gap for the absorption filler limits the thermal capacitance of stack 144 as compared to the use of a dielectric in the gap.

Consideration of the following description of a first preferred embodiment method of fabrication will provide further understanding of the first preferred embodiment bolometer and array characteristics and features. The first preferred embodiment method is illustrated in Figures 8a-g and includes the following steps.

(a) Form load resistors 143, buffer amplifiers 120, capacitors 122, addressing circuitry, metal interconnections and protective oxide 190 in silicon substrate 142 by standard CMOS processing. Photolithographically form circular openings 194 of diameter two microns in oxide layer 190 for contacts from interconnects 156 and 158 down to bias voltage 182 and load resistors 143 and capacitors 122. Sputter on 3,000 Å thick aluminum 192 which fills the openings in oxide 190 as shown in dotted lines in Figure 4b, and pattern and etch aluminum 192 to isolate interconnect contacts from ground plane; see Figure 8a in which all of the circuitry below the oxide level 190 has been omitted for clarity.

(b) Spin on layer 196 of photoimageable polyimide to a thickness of 2.5 microns on aluminum layer 192, and expose and develop a pattern of circular openings 198 about two microns in diameter for interconnects 156 and 158. Next, bake to fully imidize polyimide 196. See Figure 8b in which the opening in oxide 190 has been suppressed for clarity. The interconnects 156 and 158 need not be located directly over openings 194, so the nonplanarity of aluminum 192 at openings 194 is not a problem.

(c) Sputter deposit *in situ* the layers that will form stack 144. In particular, place targets of oxide, titanium, and boron-doped silicon in a three target RF sputtering system. First sputter deposit a 500 Å thick oxide layer 154 from the oxide target in an argon atmosphere, next sputter deposit a 100 Å thick layer 152 of TiN from the titanium target in an argon/nitrogen atmosphere, then sputter deposit 500 Å thick layer 150

of boron-doped hydrogenated amorphous silicon from the boron-doped silicon target in an argon/hydrogen atmosphere, then another 100 Å thick TiN layer 148, and lastly another 500 Å thick oxide layer 146. See Figure 8c, and note that polyimide 196 can withstand processing temperatures up to 300 degrees C which may be required during the amorphous silicon deposition to insure a low density of gap states and correspondingly large conductivity activation energy.

(d) Spin on layer 200 of photoresist and expose and develop it to define stack 144 plus electrode gap 172 for each pixel. Plasma etch with the patterned photoresist 200 as etch mask in a plasma of $\text{CF}_4 + \text{O}_2$; this plasma etches oxide, TiN, and silicon and is stopped in the amorphous silicon layer by endpoint detection of SiF^+ species in the reaction products. See Figure 8d.

(e) Strip photoresist 200 and spin on second layer 202 of photoresist and expose and develop it to define stack 144 without electrode gap 172. Plasma etch with the patterned photoresist as etch mask in a plasma of $\text{CF}_4 + \text{O}_2$ to complete removal of the amorphous silicon, TiN, and oxide layers to form stack 144. This etch does not attack aluminum 192 and is stopped in polyimide 196 by endpoint detection of CO^+ ; see Figure 8e. Also, forming leads 170 and 174 from the same stack 144 as the temperature dependent resistor simplifies the processing.

(f) Strip second photoresist 202 and spin on third layer 204 of photoresist and expose and develop it to define the contacts 176 and 178. A wet etch of oxide 146 in a 10% solution of HF and with patterned photoresist 204 as etch mask; stops on TiN 148. The wet etch is isotropic and undercuts photoresist 204; see undercuts 206 in Figure 8f. A light photoresist reflow sags photoresists 204 back to TiN 148 and eliminates the undercut overhang. Note that extreme selectivity in the etch is required because the TiN is only 100 Å thick, so if a sufficiently selective anisotropic plasma etch were used, then the undercut and consequent photoresist reflow would be avoided.

(g) Sputter deposit 5,000 Å layer 210 of Ti:W (an alloy of about 10% titanium to avoid the brittleness of pure tungsten) over patterned third photoresist 204. Spin on fourth layer 208 of photoresist and expose and develop it to define interconnects 156 and 158. Plasma etch the Ti:W in SF_6 with patterned fourth photoresist 208 as etch mask; this etch stops on photoresist 204. See Figure 8g.

(h) Spin on PMMA (polymethylmethacrylate) for protection and saw the silicon wafers containing the bolometer arrays into chips. Spin and spray the chips with chlorobenzene to remove the PMMA. Plasma ash the third and fourth photoresist layers 204 and 208 together with the polyimide layer 196. This completes the chips except for bonding and packaging. Note that in Figure 5a plasma etch access holes are shown in stack 144; these holes are etched through the oxide, TiN, and amorphous silicon layers when stack 144 is being formed and these holes reduces the amount of time required for the isotropic plasma ashing of the polyimide layer under stack 144.

Estimates on the performance of bolometer 140 with a chopping frequency of 30 Hz for chopper 104 can be computed as follows. First estimate the heat capacity C of stack 144 (taken to be 50 microns square) as 7.6×10^{-10} joules/degrees C by estimating the volumetric specific heats of oxide, TiN, and a-Si:H to be 1.7, 2.34, and 1.72 joules/cm³-degree C, respectively. Next, estimate the thermal conductivity, K , of leads 170 plus 174 as 4.6×10^{-7} watts/degree C by estimating the thermal conductivity of oxide, TiN, and a-Si:H to be about 0.02, and 0.84 watts/degree C, respectively, and the length and width of leads 170 and 174 to be 20 microns and 1 micron. Then inserting these values into $1/\sqrt{\omega^2 C^2 + K^2}$ yields:

$$T_{\omega}/\epsilon P_0^2 = 1.8 \times 10^6 \text{ degrees C/watt}$$

and the time constant $\tau = C/K = 1.7 \text{ msec}$.

Presume a 1 degree C scene temperature difference in the 8-12 micron wavelength range (i.e., warm body 116 is 1 degree warmer than his surroundings and both have emissivities of 1.0 in 8-12 microns), an infrared lens system 102 with $f/1$ optics, and an irradiance level of 5.16×10^{-5} watts/cm². Further presume $\epsilon = 0.5$ (including the quarter wavelength filter with ground plane 192 would increase this to $\epsilon \approx 1.0$), then the temperature fluctuation magnitude T_{ω} equals 0.76×10^{-3} degree C. Inserting this into the expression for the signal voltage amplitude *supra* yields a signal voltage amplitude of 78 μV (13 μV per volt of bias). This level of operation provides a total power dissipation of about 1 watt in array 106 and a mean temperature rise of about 17 degrees C ($T_m - T_0$).

Noise estimates for bolometer 140 may be computed with the foregoing presumed parameters by following the 1984 Liddiard reference cited in the Background. This yields a noise equivalent power (NEP) from Johnson and thermal fluctuation noise of about 2.5×10^{-11} watts. This translates to a noise equivalent temperature difference (NETD) of 0.02 degree K.

Further preferred embodiment bolometers and arrays are illustrated in plan views in Figures 9a-e; these plan views are analogs of Figure 5a for bolometer 140 and show the suspended resistor, leads, and interconnect to the underlying substrate containing the load resistor, signal voltage amplifier, and addressing. In particular, the upper electrode gap analogous to gap 172 of bolometer 140 is indicated by a broken

line 272 in each of Figures 9a-e, and the the interconnects to the substrate analogous to interconnects 156 and 158 of bolometer 140 are indicated by 256 and 258. Similarly, the analogs of leads 170 and 174 are indicated by 270 and 274; note that in the preferred embodiment of Figure 9d the leads are bifurcated.

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MODIFICATIONS AND ADVANTAGES

Various modifications of the preferred embodiment devices and methods may be made while retaining features such as a temperature dependent resistor suspended over related processing circuitry for a high fill
10 factor and avoidance of resistor edge passivation, a temperature dependent resistor made of a radiation absorbing material on a transparent high temperature coefficient material, a quarter wavelength filter absorber, and *in situ* deposition of resistor materials for uniform and reproducible resistors.

For example, the dimensions, shapes, and materials of the stack forming the temperature dependent resistor may be varied, although the absorption efficiency depends upon the sheet resistance and
15 processing-compatible materials are needed for manufacturability; the contact arrangement for the temperature dependent resistor could be varied such as one top contact and one bottom contact so the electrode gap would not be needed, or two bottom contacts; the passivation layer (oxide 146 and 154 in preferred embodiment 140) on the temperature dependent resistor contacts could be omitted and thereby lower the thermal capacitance; the edges of the temperature dependent resistor could be passivated to limit surface
20 leakage (such as by oxide deposition after step (e) in the first preferred embodiment method); the air bridge interconnects 156 and 158 of bolometer 140 could be replaced by direct interconnect metal if the temperature dependent resistor is insulated to avoid the interconnects shorting out the top and bottom contacts; and the amorphous silicon resistive layer 150 could be heavily doped adjacent to the titanium nitride layers 148 and 152 in order to lower specific contact resistance and contact noise, of course the
25 thickness of the high resistivity amorphous silicon will remain constant.

The invention provides the advantage high fill factor arrays.

Claims

30

1. An array of bolometers for detecting radiation in a spectral range, comprising:
 - (a) a substrate including an array of bolometer circuit elements adjacent a first surface;
 - (b) an array of resistors, each of said resistors spaced from said surface, oriented to receive radiation, and located over and electrically connected to a corresponding set of said circuit elements.
- 35 2. The array of claim 1, further comprising:
 - (a) a chopper for the radiation received by said resistors.
3. The array of claim 1, wherein:
 - (a) each of said resistors has leads, and said resistor plus leads are formed from a stack including a first conductive layer, a resistive layer, and a second conductive layer with said second conductive layer the
40 closest layer to said surface.
4. The array of claim 3, wherein:
 - (a) each of said resistors is mechanically connected to said surface by connections that attach to said leads on said first conductive layer; and
 - (b) said stack is supported solely by said connections.
- 45 5. The array of claim 3, wherein:
 - (a) said first conductive layer is split into two regions; and
 - (b) said two regions are connected to said corresponding circuit elements by said leads plus air bridges.
6. The array of claim 3, wherein:
 - (a) the distance from said second conductive layer to said surface is about one quarter of the
50 wavelength of the center of the spectral range.
7. The array of claim 6, wherein:
 - (a) said second conductive layer has a passivation layer;
 - (b) said surface is reflective; and
 - 55 (c) the space between said surface and said passivation layer is evacuated.
8. The array of claim 6, wherein:
 - (a) the sheet resistance of said first and second conductive layers provides at least 50% absorption in said spectral band.

9. The array of claim 3, wherein:

(a) said stack includes a first passivation layer on said first conductive layer and a second passivation layer on said second conductive layer;

(b) said first and second passivation layers are silicon dioxide;

5 (c) said first and second conductive layers are titanium nitride; and

(d) said resistive layer is amorphous silicon.

10. A bolometer for detecting radiation in a spectral range, comprising:

(a) a resistor formed from a planar stack, said stack including (i) a planar layer of resistive material with first and second surfaces, the resistivity of said resistive material depending upon temperature, (ii) a
10 first layer of a first conducting material abutting said first surface, (iii) a second layer of a second conducting material abutting said second surface, said first layer split into first and second regions which form the two terminals of the resistor;

(b) said resistor oriented to receive radiation in a spectral range; and

(c) a detector for detecting the resistivity of said resistor.

15 11. The bolometer of claim 10, further comprising:

(a) said layer of resistive material having high conductivity adjacent said first layer and second layer.

12. The bolometer of claim 10, further comprising:

(a) a chopper for the radiation received by said resistor.

13. The bolometer of claim 10, further comprising:

20 (a) a third layer of a third conducting material, said third layer parallel to and spaced from said second layer a distance equal to about one quarter of the wavelength of the center of said spectral range.

14. The bolometer of claim 13, wherein:

(a) the thickness and conductivities of said first layer and said second layer are characterized by at least 50% absorption at the center of said spectral range by said resistor.

25 15. The bolometer of claim 10, wherein:

(a) said resistive material is amorphous silicon;

(b) said first and second conducting materials are titanium nitride; and

(c) said stack is characterized by *in situ* deposition of said second layer, said planar layer, and said first layer.

30 16. The bolometer of claim 10, wherein:

(a) said two regions are connected to said detector by leads plus air bridges.

17. A method of detecting radiation in a spectral range, comprising the steps of:

(a) providing a resistive material which is substantially transparent to radiation in a spectral range;

(b) providing conducting material on said resistive material;

35 (c) providing a reflecting material spaced from said conducting material;

(d) wherein the thickness and conductivity of said conducting material and said reflecting material spacing are characterized by at least 50% absorption about the center of a spectral range; and

(e) measuring the temperature of said resistive material when exposed to radiation in the spectral range.

40 18. The method of claim 17, further comprising the step of:

(a) chopping the radiation received by said materials.

19. The method of claim 17, wherein:

(a) said conducting material is titanium nitride;

(b) said resistive material is amorphous silicon; and

45 (c) said reflecting material is aluminum.

20. A method of fabrication of an infrared imager, comprising the steps of:

(a) forming an array of bolometer circuitry at a surface of a substrate;

(b) forming a spacer layer over said circuitry;

(c) forming a resistor stack on said spacer layer;

50 (d) forming connections from said resistor stack to said circuitry; and

(e) removing said spacer layer.

21. The method of claim 20, further comprising the step of:

(a) forming a reflective layer over said bolometer circuitry prior to said forming a spacer layer.

22. The method of claim 20, wherein:

55 (a) said forming a resistor stack is by *in situ* deposition of a passivation layer, a conductive layer, a resistive layer, another conductive layer, and another passivation layer.

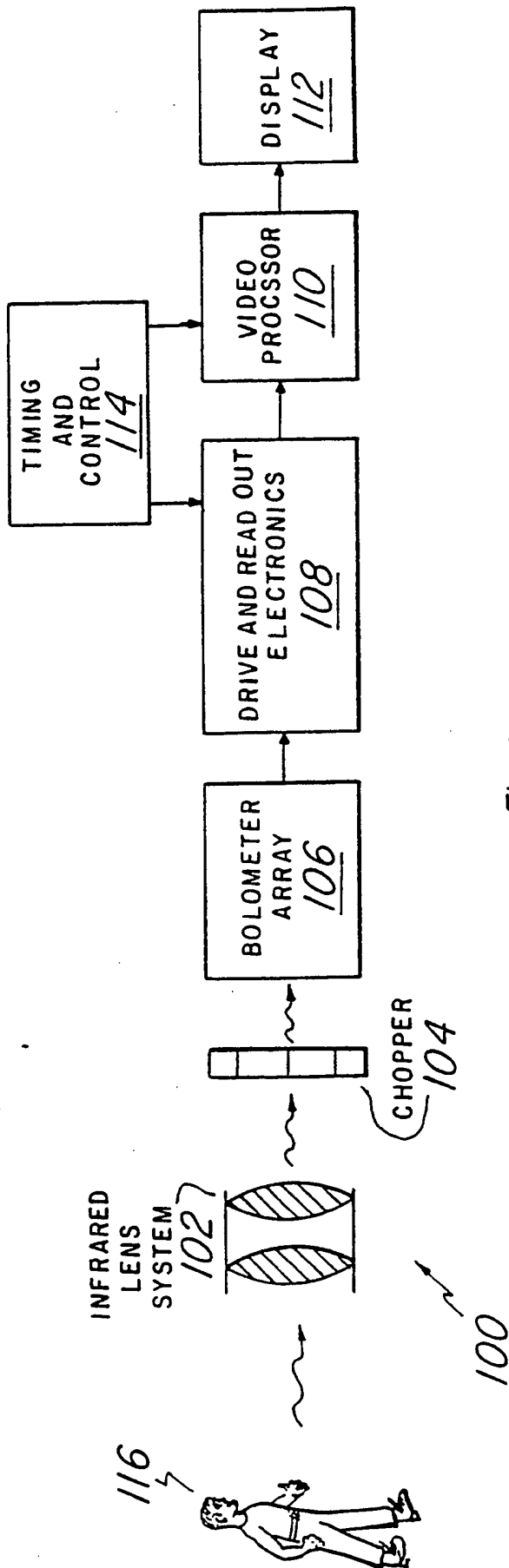


Fig. 1

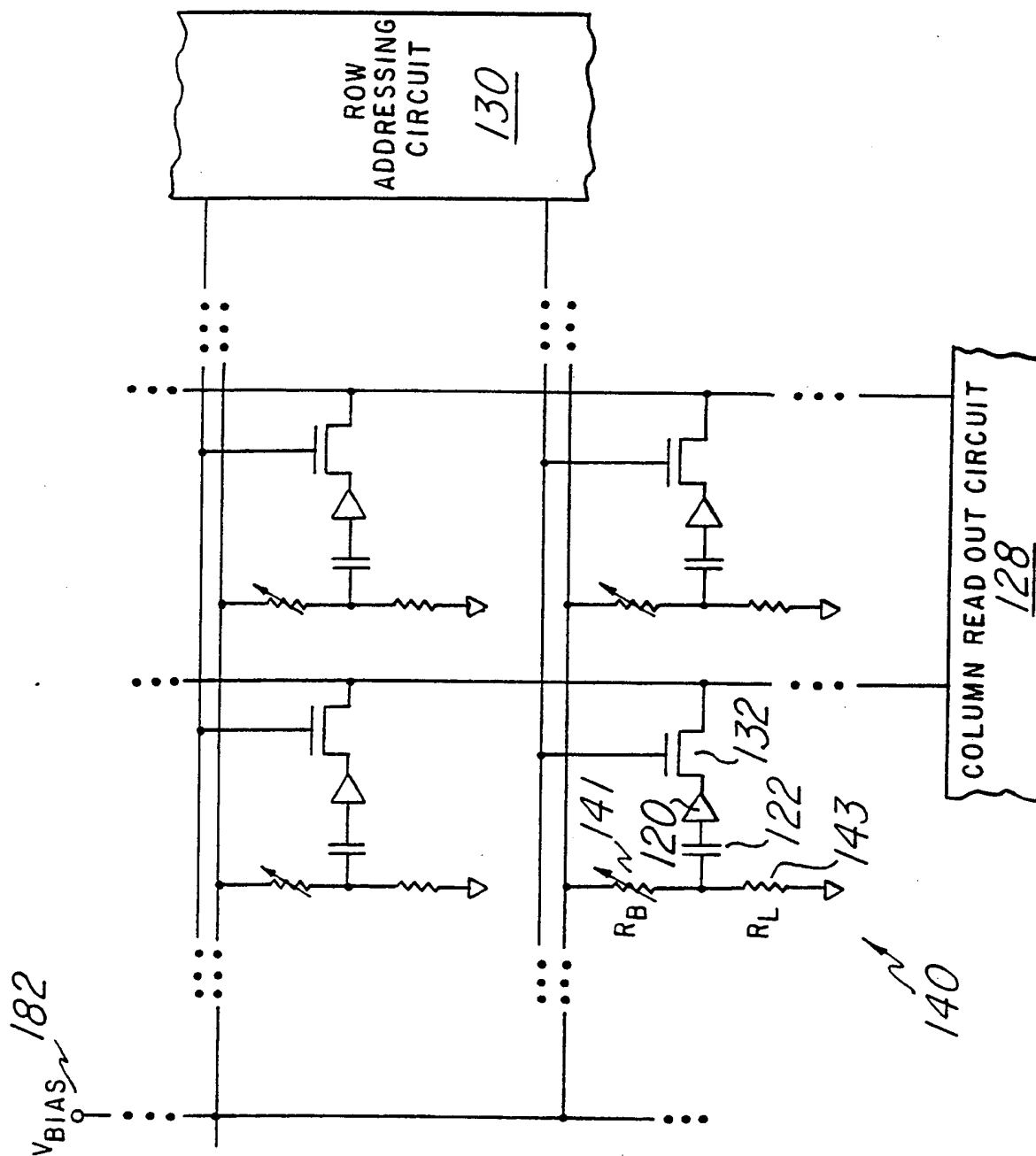


Fig. 2

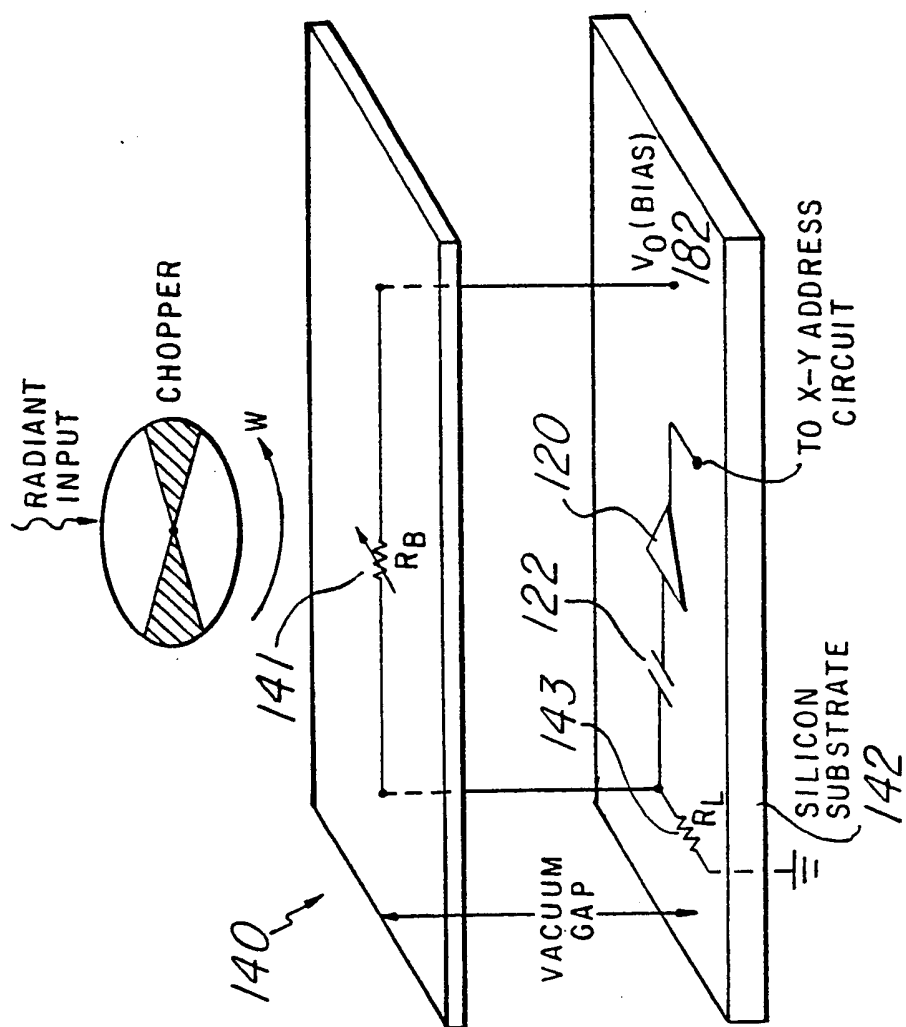


Fig.3

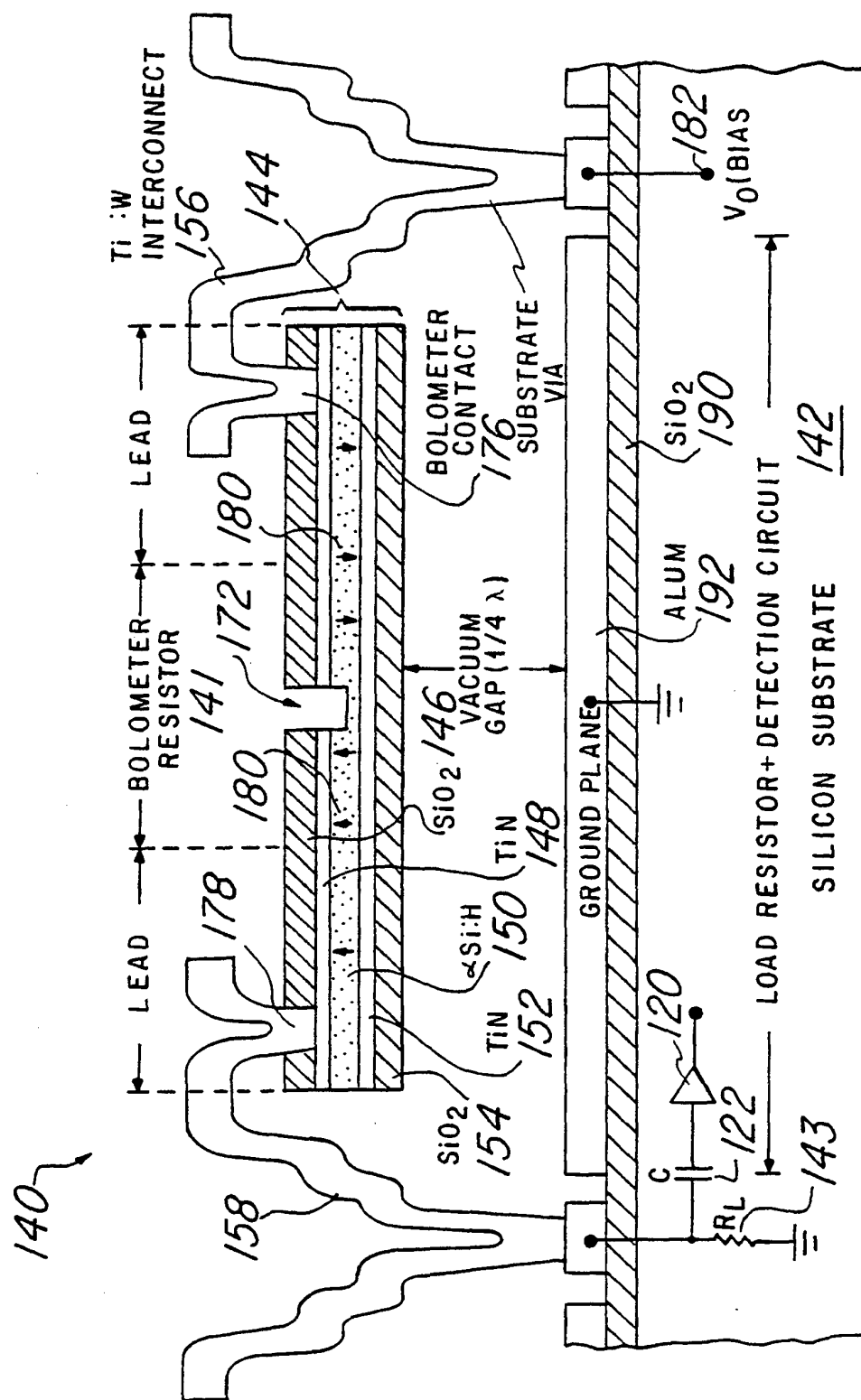


Fig. 4a

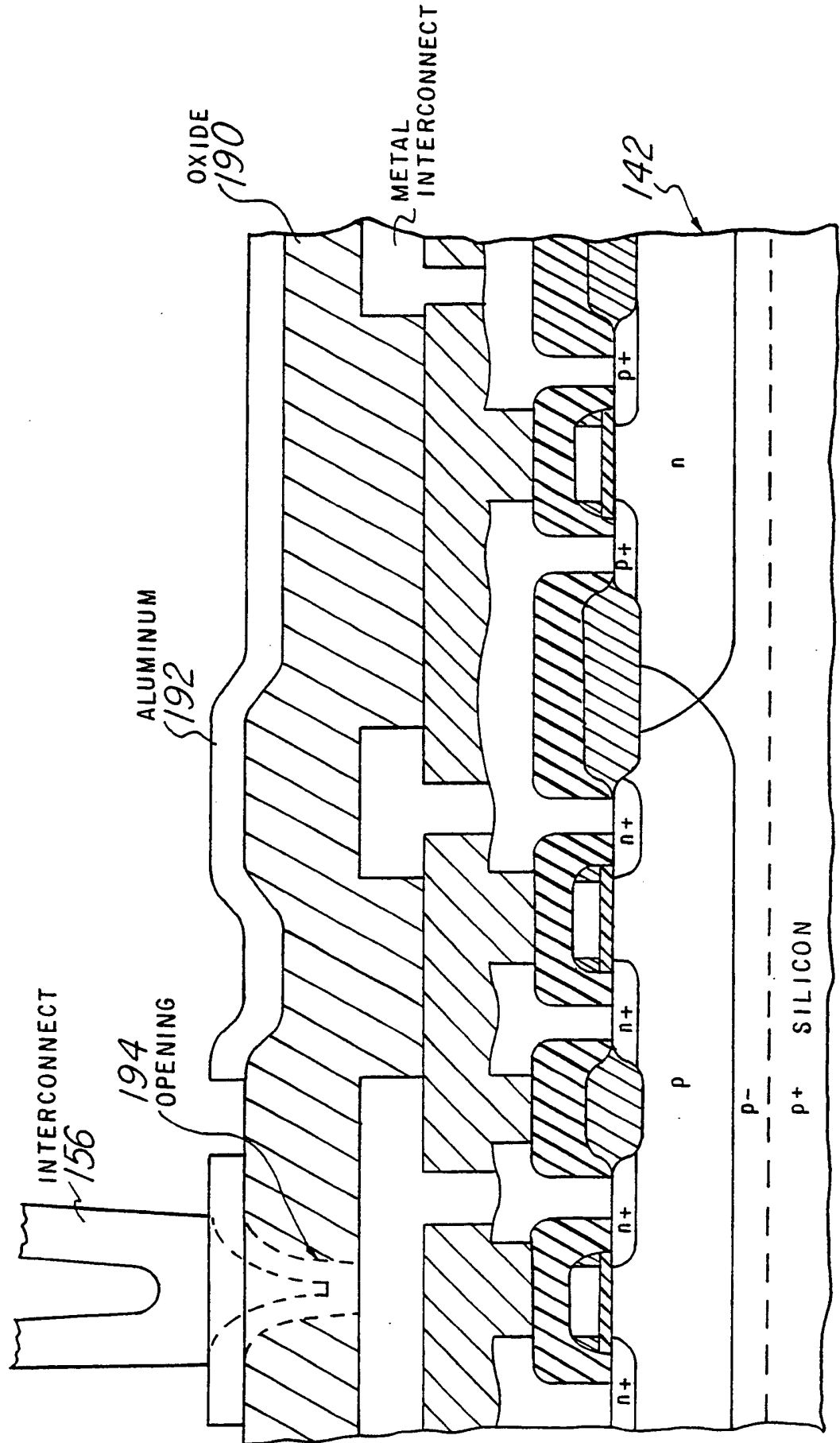


Fig. 4b

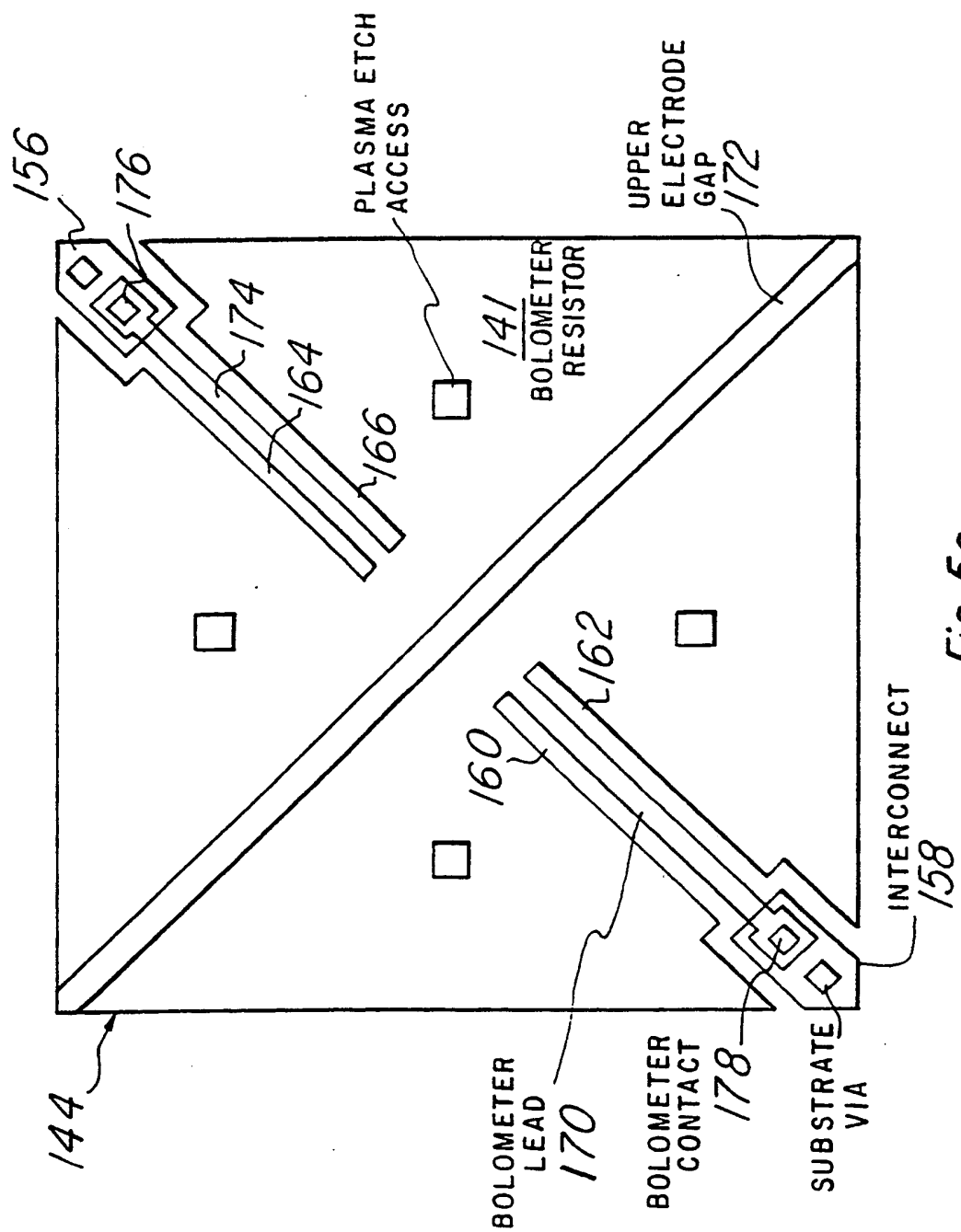


Fig. 5a

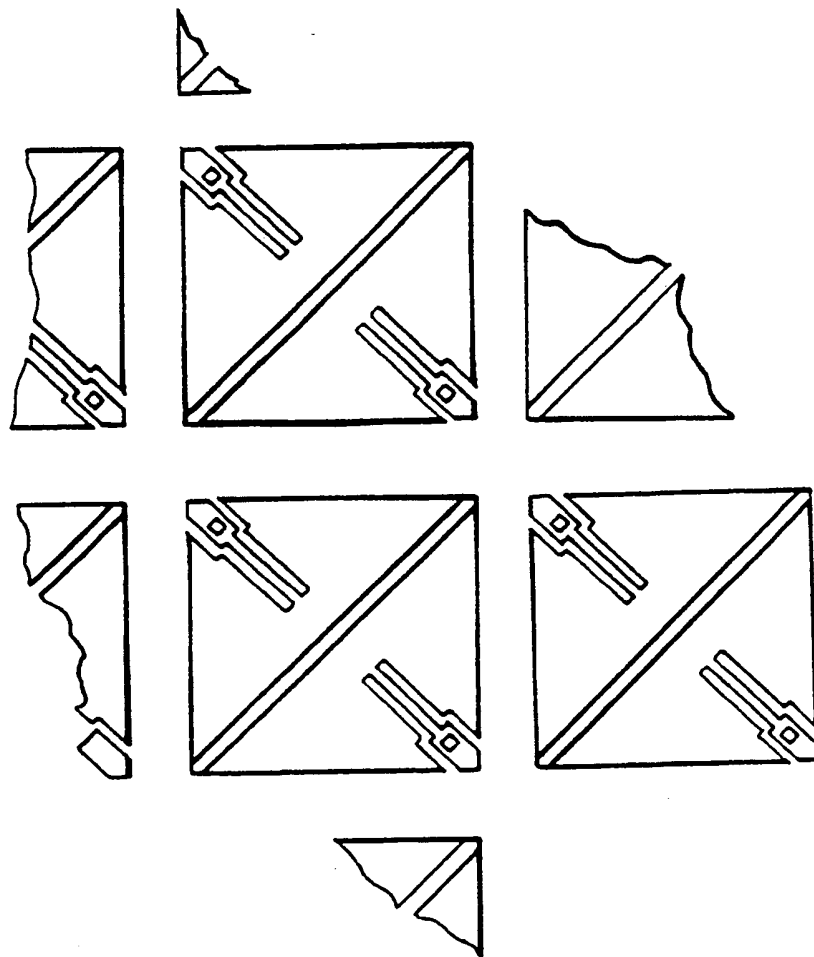


Fig. 5b

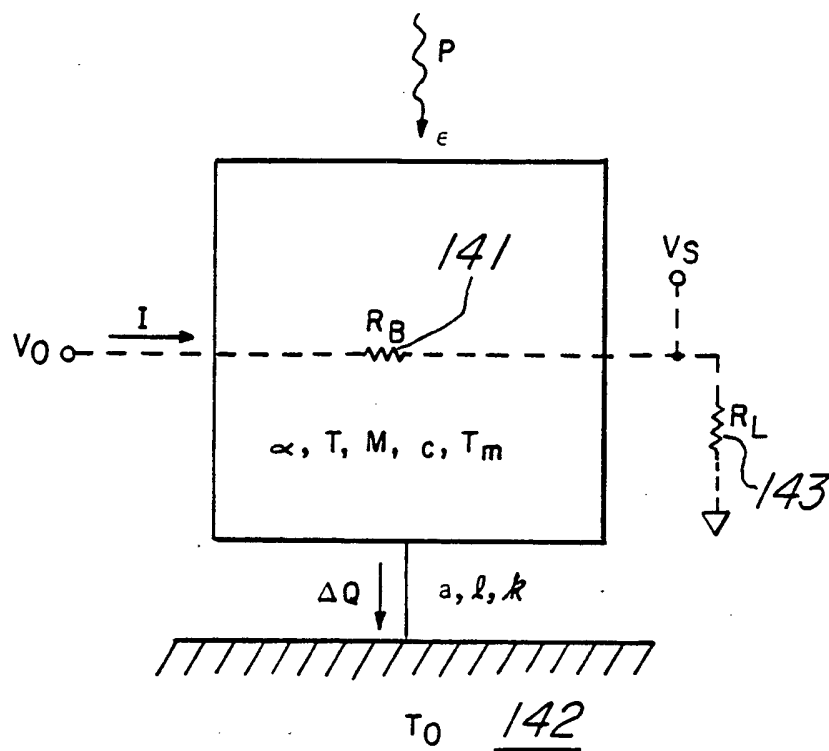


Fig. 6

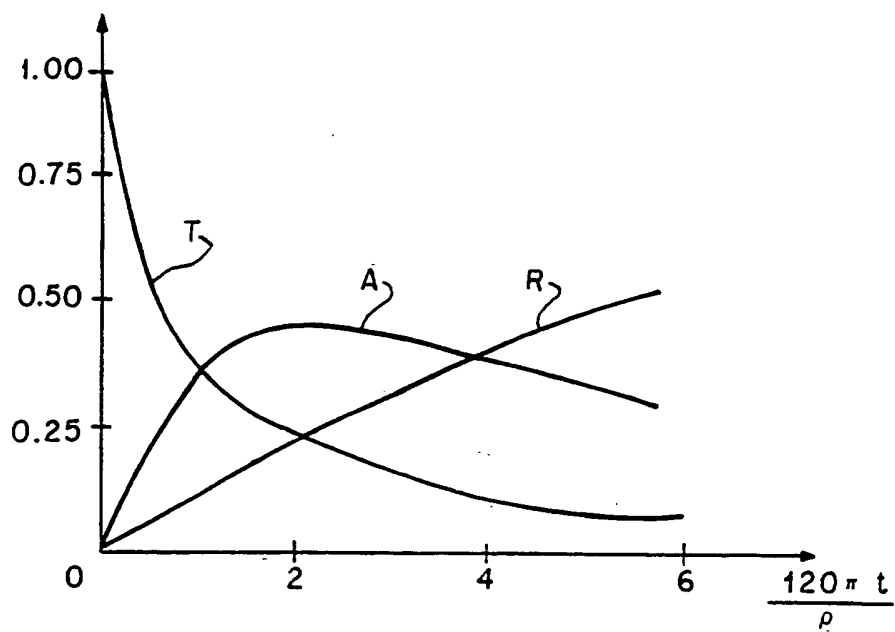


Fig. 7

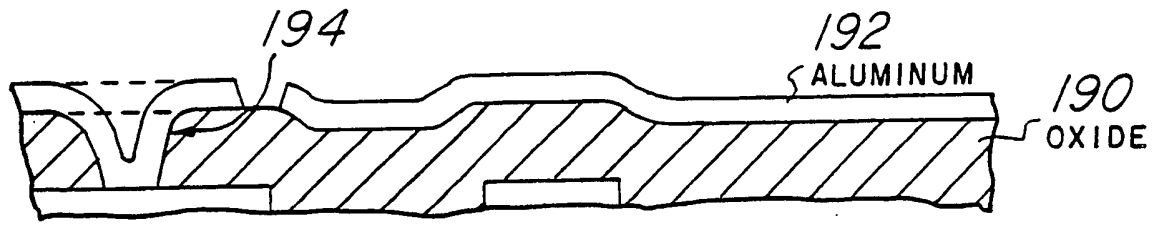


Fig. 8a

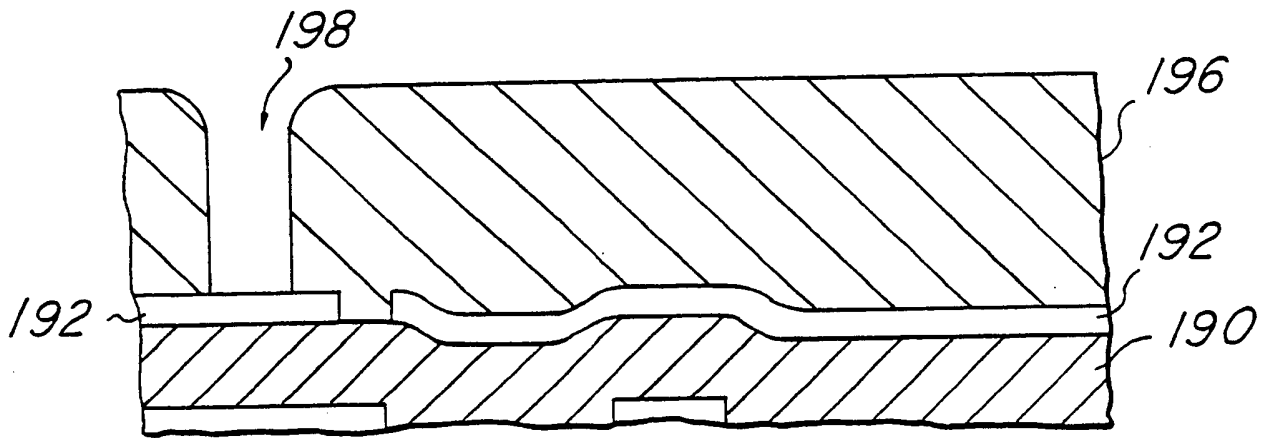


Fig. 8b

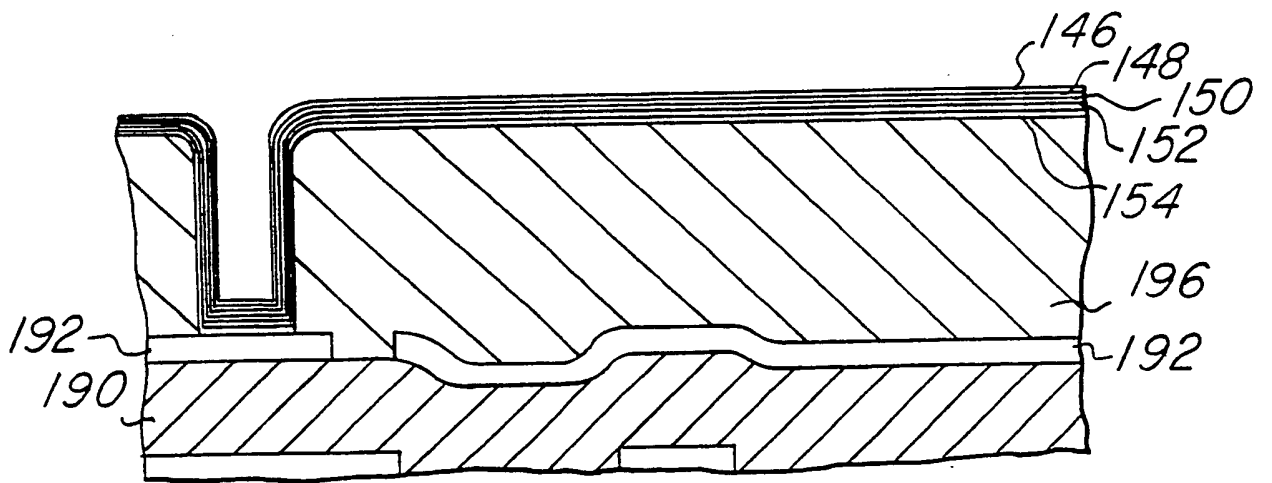


Fig. 8c

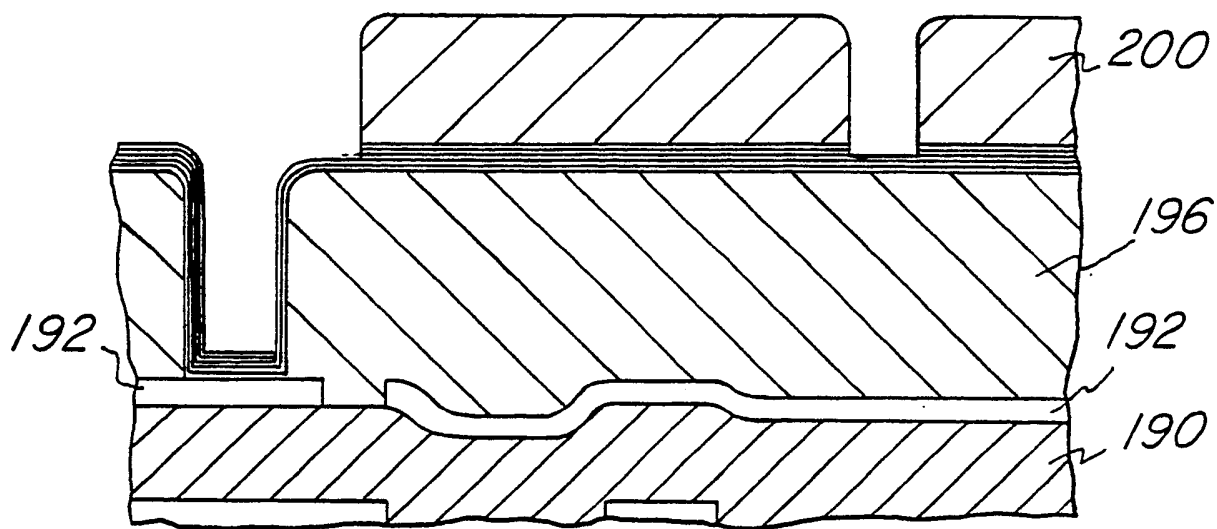


Fig. 8d

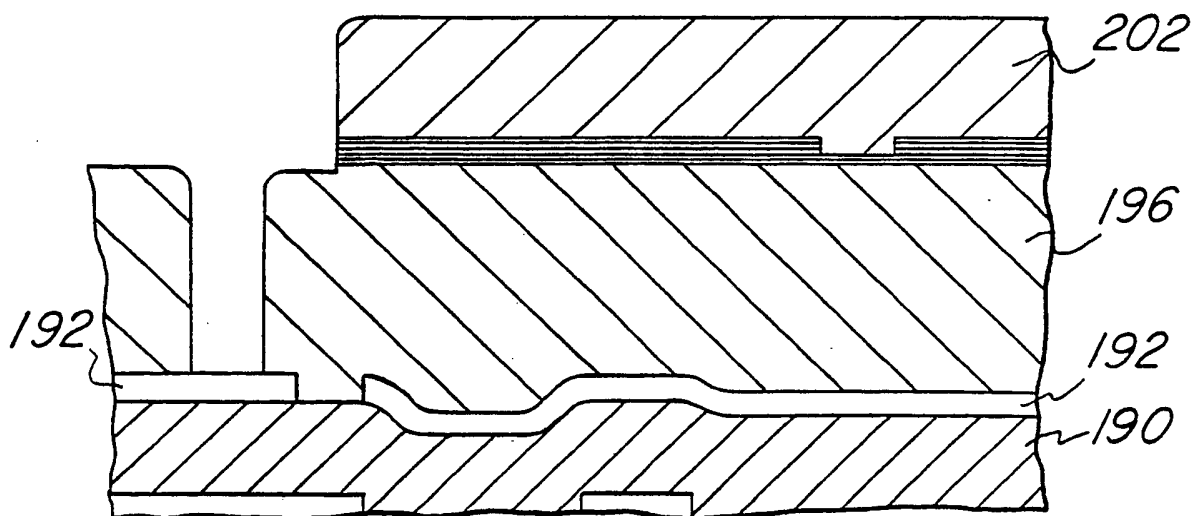


Fig. 8e

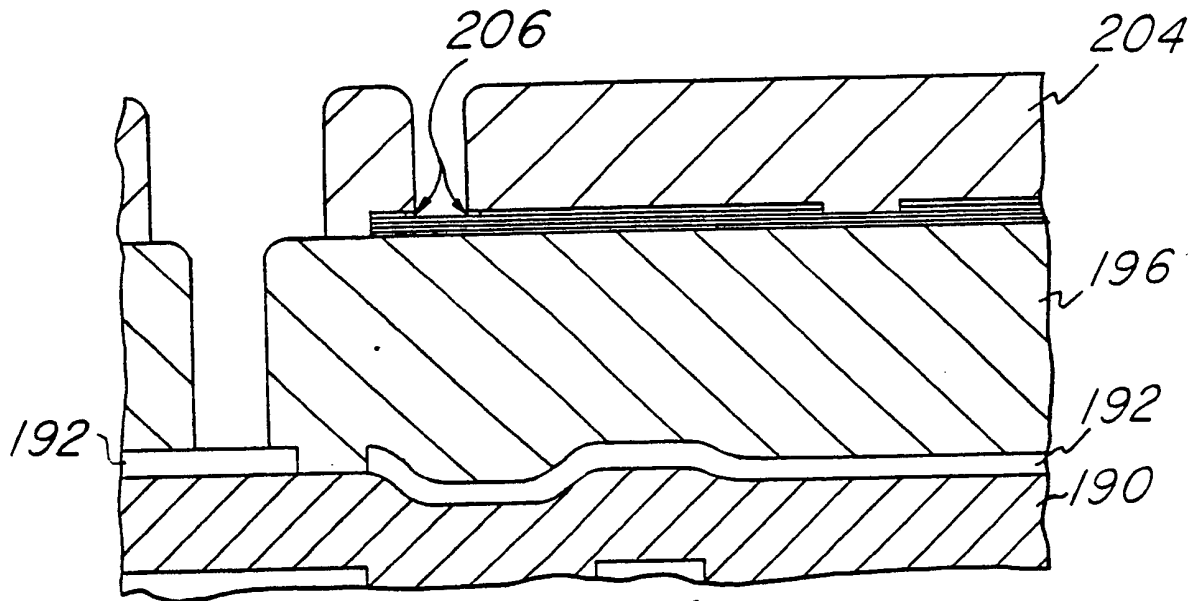


Fig. 8f

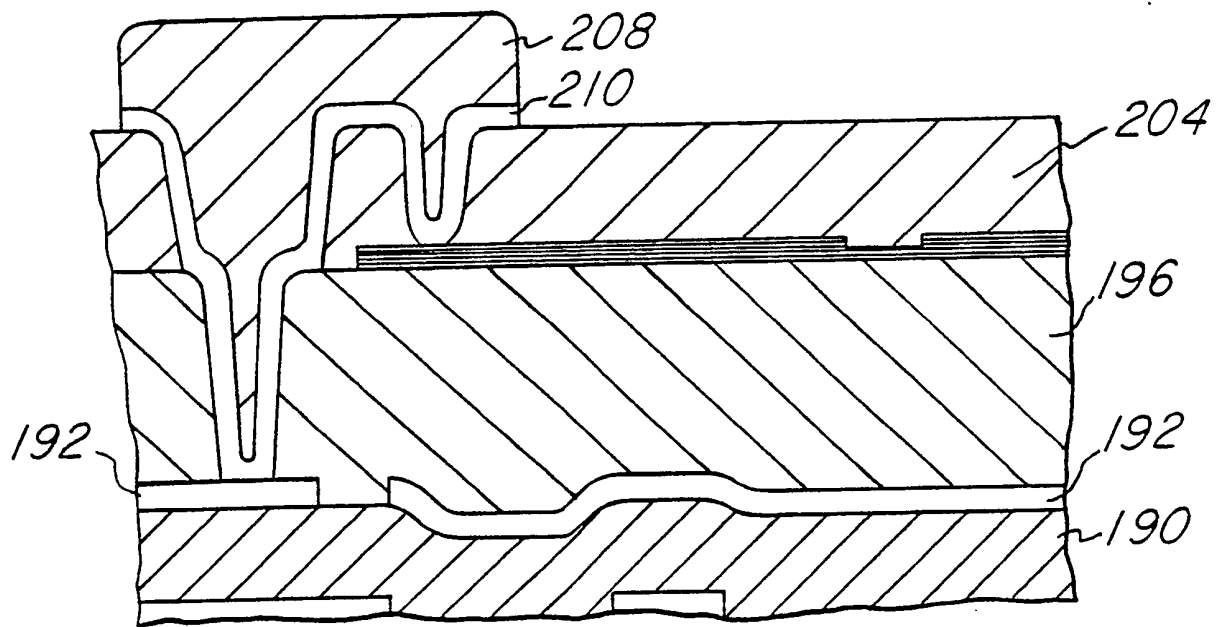


Fig. 8g

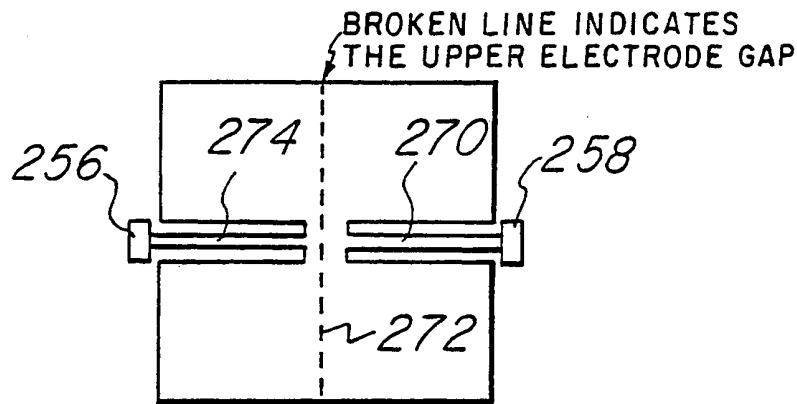


Fig. 9a

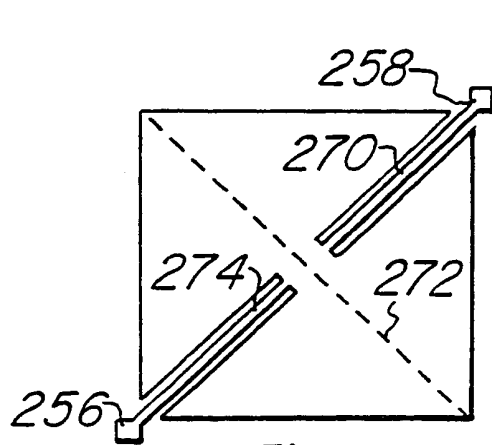


Fig. 9b

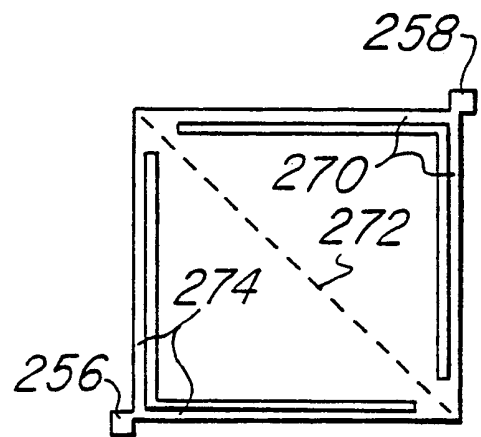


Fig. 9d

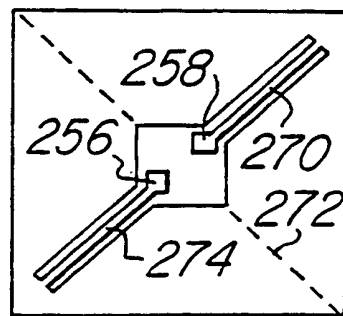


Fig. 9c

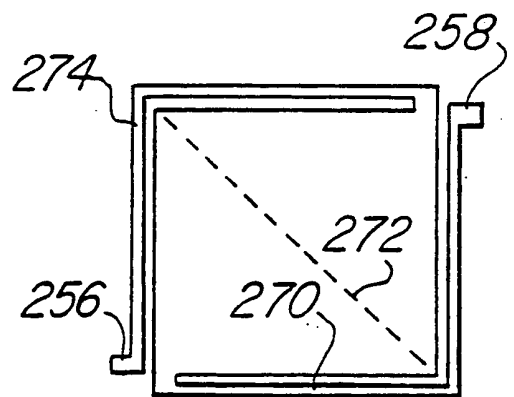


Fig. 9e

EUROPEAN PATENT APPLICATION

Application number: 89112731.8

Int. Cl.⁵: **G01J 5/20, H05K 1/16,**
//G08B13/18

Date of filing: 12.07.89

Priority: 12.08.88 US 231797

Date of publication of application:
 14.02.90 Bulletin 90/07

Designated Contracting States:
DE FR GB IT

Date of deferred publication of the search report:
 07.11.90 Bulletin 90/45

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Infrared detector.

Preferred embodiments include a monolithic uncooled infrared detector array of bolometers fabricated over a silicon substrate (142); the bolometers include a stack (144) of oxide (146), TiN (148), a-Si:H (150), TiN (152), oxide (154) with the TiN forming the infrared absorbers and resistor contacts

and the a-Si:H the resistor with a high temperature coefficient of resistivity. The resistor is suspended over the silicon substrate (142) by metal interconnects (154 and 156) and related processing circuitry is formed in the silicon substrate (142) beneath the resistor.

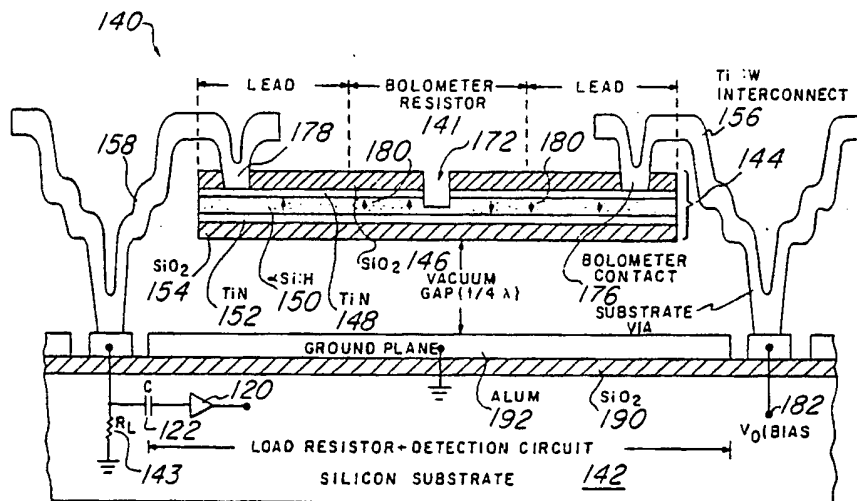


Fig. 4a

EP 0 354 369 A3



DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
X	US-A-4 754 139 (ENNULAT) * Column 3, lines 12-34; column 4, lines 44-51; column 6, lines 39-47; figure 2 *	1	G 01 J 5/20 H 05 K 1/16 // G 08 B 13/18 H 01 L 27/16
A	---	4	
D,A	US-A-4 142 207 (McCORMACK) * Abstract; column 3, lines 30-37; column 4, lines 12-19; figures 1,2 *	1,2,12,18	
D,A	INFRARED PHYSICS, vol. 24, no. 1, 1984, pages 57-64; K.C. LIDDIARD: "Thin-film resistance bolometer IR detectors" * Abstract; page 6, section: "Detector materials", paragraph 2 *	1,6-8,10,13,14,17,21	
D,A	INFRARED PHYSICS, vol. 26, no. 1, 1986, pages 43-49; K.C. LIDDIARD: "Thin-film resistance bolometers-II" * Abstract; page 47, section: "An experimental detector", paragraphs 3-5 *	1,9,15,19	
A	INTERNATIONAL CONFERENCE ON SOLID-STATE SENSORS AND ACTUATORS, 1985, pages 132-135, IEEE, New York, US; I.H. CHOI et al.: "A linear thermopile infrared detector array with on-chip multiplexing" * Abstract; page 132, final paragraph - page 133, right-hand column, line 12; figure 2 *	20,22	G 01 J H 01 L

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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 03-09-1990	Examiner THOMAS R.M.
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	



DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	APPLIED OPTICS, vol. 23, no. 6, March 1984, pages 910-914, New York, US; P.M. DOWNEY et al.: "Monolithic silicon bolometers" * Abstract; page 910, right-hand column, line 17 - page 911, right-hand column, line 8 *	20,22	
A	INTERNATIONAL ELECTRON DEVICES MEETING, Washington, D.C., 1st - 4th December 1985, pages 436-439, IEEE, New York, US; H. ITO et al.: "a-Si:H TFT array driven linear image sensor with capacitance coupling isolation structure" * Abstract; page 439, paragraph 1; figure 2 *	9,15,19	
A	EP-A-0 272 731 (PHILIPS) * Abstract; column 1, line 1 - column 2, line 17 *	1,6,7, 13,17, 21	
P,A	PATENT ABSTRACTS OF JAPAN, vol. 12, no. 314 (E-649)[3161], 25th August 1988; & JP-A-63 78 581 (FUJITSU) 08-04-1988 * Whole abstract *	9,15,19	
The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
Place of search THE HAGUE		Date of completion of the search 03-09-1990	Examiner THOMAS R.M.
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